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AUG 27 2004

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:)
)
Hiroyuki Nitta et al.)
)
Serial No.: 09/883,210)
)
Filed: June 19, 2001)
)
For: Semiconductor Device Having A)
Wiring Layer Of Damascene)
Structure And Method For)
Manufacturing The Same)

Group Art Unit: 2815
Examiner: J. Nguyen
Atty. Dkt. No. 01701.00086

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RESPONSE TO ELECTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is responsive to the Restriction Requirement mailed July 30, 2004.
Applicants hereby elect Group I in response to the restriction requirement.


Applicants next elect Species V, relating to Figures 6A-6E, relating to the election requirement.

These elections are made without traverse.

Claims 1-4 and 10-15 correspond to Species V.

Applicants reserve the right to file a divisional application directed to the non-elected invention. Please charge any fee associated with the filing of this paper to our Deposit Account No. 19-0733.

Respectfully submitted,

By: 
for Joseph M. Potenza
Registration No. 28,175

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Dated: August 27, 2004